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				Application Number	10/816,322
				Filing Date	March 30, 2004
				First Named Inventor	Maria R. Tursi
				Art Unit	2816 2816
Examiner Name	Not Yet Assigned <i>TON</i>				
Sheet 1 of 1	Attorney Docket Number		08211/1200663-US1/P05913		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
MT	CA	K. Nagaraj, D. Martin, M. Wolfe, R. Chattopadhyay, S. Pavan, J. Cancio and T.R. Viswanathan, "A Dual-Mode 700-Msamples/s 6-bit 200-Msamples/s 7-bit A/D Converter in a 0.25-um Digital CMOS Process", IEEE Journal of Solid-State Circuits, Vol. 35, No. 12, pp. 1760-1768, December 2000	
MT	CB	C. Eichenberger and W. Guggenbuhl, "Dummy Transistor Compensation of Analog MOS Switches", IEEE Journal of Solid-State Circuits, Vol. 24, No. 4, pp. 1143-1146, August 1989	

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